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memory external correction signal

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Error correction system in a teletext system - US Patent 4672612 ...

An error **correction** system comprising (a) a buffer **memory** (19) for storing an ... error **correction** circuit, and an address **signal** from an **external** circuit, ...
www.patentstorm.us/patents/4672612/claims.html

Method for automatically setting up a television camera - US ...

A method as claimed in claim 1, further comprising the step of temporarily storing the **external** test pattern **correction signal** in an **auxiliary memory**. ...
www.patentstorm.us/patents/4513319/claims.html

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Memory with function test of error detection/correction device ...

by T Matsutara - 1990 - [Cited by 1](#) - [Related articles](#)

correction means receiving the stored data from said **memory** means and the decoded first error-detection/**correction signal** for correcting the stored data. ...
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MEMORY DEVICES AND SYSTEMS INCLUDING ERROR-CORRECTION CODING AND ...

The **memory** device of claim 1, wherein the data and the error **correction** codes are ... EC driver which further receives **external** error **correction** codes. in the data latency control **signal** CTRL_DATA and the error **correction** code ...
www.faqs.org/patents/app/20080307265 - [Cached](#)

Error detection and correction - Wikipedia, the free encyclopedia

5 See also; 6 Notes; 7 References; 8 **External** links over a channel that has a certain error probability or **signal**-to-noise ratio (SNR). Error-correcting **memory** controllers traditionally use Hamming codes, although some use ...
en.wikipedia.org/wiki/Error_detection_and_correction - [Cached](#) - [Similar](#)

External Memory Interfaces in Cyclone Devices

The DQ **signal** is sent to the **memory** device on both edges of the in-phase ... More information about the **external memory** interface in Cyclone devices is ...
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Duty cycle correction circuit and delay locked loop circuit ...

However, because the internal circuit of the **memory** device delays the clock input The duty cycle ratio of the **external** clock EXT_CLK is not 50:50. the duty cycle ratio detector 155 outputs the **correction signal** CTRL turning on ...
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(WO/2009/135664) DIGITAL CONTROLLER FOR AN AUTOMOTIVE STEERING ...

an iteration **memory** for storing the second motion **correction signal** as a stored motion ... computing a control **signal** from an **external** reference **signal**, ...
www.wipo.int/pctdb/ja/ia.jsp?ia=EP2009%2F003240&ia... - [Cached](#)

South Korean Inventor Develops Clock Signal Distorted Duty ...

The duty **correction** circuit includes a repeater that generates a clock ... have developed an **external** clock **signal** receiving loop...of an **external** clock ... South Korean Inventor Develops **Memory** Clock **Signal** Frequency Changing Apparatus ...

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Exciting **signal** generator for SSTs - Elsevier

by I Zemanek - 2003

... by the ideal sinusoidal **signal**—digital **signal** prototype $s(n)$ (7)—stored in the **external memory** ROM (Fig. 1). ... Computation of the **correction signal**: ...

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